${f S}_{ m olomon}$ SSD1963 Display Controller Kit

Part #: SSD1963EVK Ultima R4.1

Packing list

Item	Description	Qty	Picture
1	SSD1963 Evaluation Kit Ultima Rev4.1	1	
			Figure 1.1
2	EVK PIC28PDIP R1.0 is included for out-of- the-box demo with breakout for all interface signals in 2.54mm pin headers	1	Figure 1.2
	2x20 2.54mm pin header	1	0
	3x3 2.54mm pin header	1	
	2x2 2.54mm pin header	2	
	1x3 2.54mm pin header	1	
3	Universal TFT Adapter PCBs for your choice of TFT screens	1	Solder a connector here for your screen Strip wires TFT signals Figure 1.3
4	Jumper cables for 2.54mm pin headers, a pack of 40 pieces included	1	jumper cables incl. Figure 1.4

This product combination is suitable for users who want to adapter SSD1963QL9 for his own MCU and TFT screen. The MCU on EVK PIC28PDIP R1.0 is a Microchip PIC18F24J10-I/SP with demo program preloaded for 4.3" TFT panel (part# TY430TFT480272Rev04). One may probe the PCLK, VSYNC, and HSYNC signals from the universal TFT adapter for presence of timing signals (PCLK, VSYNC, and HSYNC, etc) to prove the boards are working. Afterwards, remove PIC18F24J10-I/SP for another target board.

All MCU-to-SSD1963QL9 interface signals have been *"breakout'ed"* on EVK PIC28PDIP R1.0. Remove the 28-pin PIC18F24J10-I/SP from EVK PIC28PDIP R1.0 and wire up your target board by following the example on Figure 1.5.

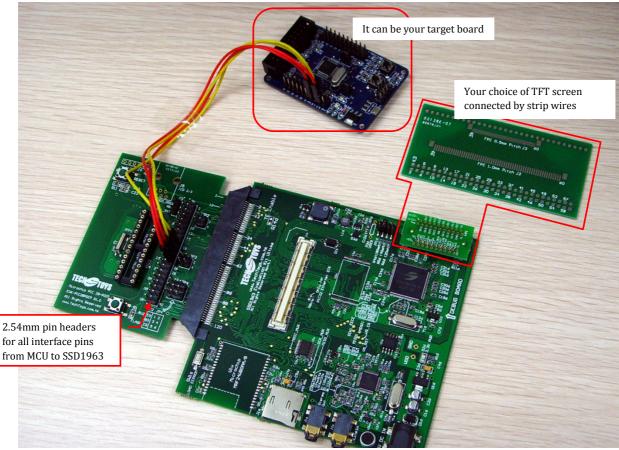


Figure 1.5 Setup for connections