# **Evaluation board for NXP LPC2103**

**User Guide** 

## SOFTWARE

Download from KEIL web site at <u>http://www.keil.com/demo/</u> for ARM evaluation software. Limitations to this evaluation copy have been summarized on the web page

<u>http://www.keil.com/demo/limits.asp</u>. At time of writing, the version was 3.22a. Double click on the icon mdk322a.exe and follow the default installation path.



After installation, launch µVision 3 from its icon on the desktop and browse to your project directory containing a valid µVision project. You may download from our web site at <u>http://www.techtoys.com.hk/ARM boards/LPC2103 Eval 1A/LPC2103 Eval 1A.htm</u> for some ready-to-run projects including HelloWorld project, LED blinky project, as well as simple color changing demonstration for 2.4" TFT color LCD, and 2.8" TFT color LCD.

Let's take the 2.4" TFT example as a demo for now. Unzip the file to any place in your hard-drive, under *Project*—*Open Project*, browse to the \*.uv2 file ILI9320 Demo.uv2 and click open. Right away you will see the project setting with source file as shown in the screen shot below.



Before getting the project compiled, please take a look at the project setup to understand what is going on with various options. Some of them are keys to successful program compilation and linkage. Highlight and right click on the parent folder under *Project Workspace* at the left panel.



Open Options for Target 'Target 1' you will see project options as follows.

NXP (fou	nded by Pl	hilips) LPC2103	3		l ⊢Co	ide Genera	ation		
		2	Xtal (MHz):	2.0	1	ARM-Mod	в	-	
Operat	ing system:	None		-	Г	Use Cro	ss-Module Opti	mization	
				_	Г	Use Mid	croLIB	🔲 Big Er	ndian
- Read/	Only Memo	ory Areas			-Read/	Write Mer	norv Areas		
default	off-chip	Start	Size	Startup	default	off-chip	Start	Size	Nol
Г	ROM1:			С	Г	RAM1:			Г
Г	ROM2:			с	Г	RAM2:			Г
Г	ROM3:			C	Г	RAM3:			Г
	on-chip					on-chip			
	IROM1:	0x0	0x8000	(	~	IBAM1:	0x40000000	0x2000	
Γ	IROM2:			C	Г	IRAM2:		-	Г

Important configuration:

- 1. *Device, Target, Output, Linker, and Utilities*. Under the *Device* tab, it states that NXP LPC2103 microcontroller has been chosen.
- 2. *Target* tab shows the Xtal frequency value, Read/Only Memory Areas, and Read/Write Memory Areas. These parameters vary from individual hardware and choice of microcontroller. Because we are using an external crystal of 12MHz, we need to input 12.0 under Xtal text box.
- 3. *Read/Only Memory Areas* contains the Flash start address and size. LPC2103 has 32KB on-chip Flash ROM therefore its size reads 0x8000. Its starting address is 0x0 from ARM architecture.

- 4. *Read/Write Memory Areas* defines the starting address and size of static RAM of LPC2103. It is always possible to define different values for on-chip Flash ROM and RAM for a certain microcontroller (e.g. LPC2103) and get the project successfully compiled. However, its resultant hex file may not be able to run correctly.
- 5. Under the *Output* tab, check the check-box Create HEX File; otherwise, no hex file will be created upon successful compilation.

Options for Target 'Target 1'	
Device Target Output Listing User C/C++ Asm Linker Debug Utilities	
Select Folder for Objects Name of Executable: ILI9320 Demo	
Create Executable: .VLI9320 Demo  Debug Information  Create HEX File  Browse Information  Create Library: .VLI9320 Demo.LIB	☐ Create Batch File
OK Cancel Defaults	Help

6. Under the *Linker* tab there are important parameters for non-volatile memory (R/O Base) and static RAM (R/W Base) base addresses. One should enter the correct values for these boxes otherwise, the program will not compile or not running properly.

7.	There are other
	important options
	for Debug and Flash
	programming
	Utilities which I will
	leave them until the
	introduction on
	JTAG debugger.

∏ Ma	ke RW Sections Position Independent	R/O Base	x 0x0000000	6
I Ma	ke RO Sections Position Independent n't Search Standard Libraries	R/W Bas	e 0x40000000	
🔽 Rej	port 'might fail' Conditions as Errors	disable Warning:	x ]	
File				
File				
File				
File Misc controls				2

Click *OK* to quit the setup.

Going back to *Project Workspace* and right click on parent folder *Target 1* again and select *Manage Components*. You will see under *Folders/Extensions* the *Tool Base Folder* which is C:\Keil\ARM\ and the compiler folder is actually under BIN31\. These are important paths that should not be confused; otherwise, the IDE will not be able to point to the correct compiler and linker.

Development Tool Fo	olders		Default File Extensions:		
Use Settings from	TOOLS.INI:		C Source:	*.c	
Tool Base Fold	er: C:\Keil\ARM\		C++ Source:	*.cpp	
BIN: C:\Keil	ARM/BIN/		Asm Source:	*.s*; *.src; *.a*	
INC:			Object:	*.obj	
LIB:			Library:	*.lib	
Regfile:			Document:	*.txt; *.h; *.inc	
Use RealView Compiler Use GNU Compiler	RealView Folder: BIN31\ GNU-Tool-Prefix: Cygnus Folder: C:\Cygnus\	_			

It is interesting to note that under the option Use *RealView Compiler* there is another option as **Use GNU Compiler**. Evaluation version of RealView limits to 16KB of code size. However, the GNU ARM tools (compiler, assembler, and so on) that are provided are not limited or restricted in any way except that one must not use it for commercial product. Before you may use the GNU compiler, download and install from the same web page that you have downloaded the RealView compiler. The default installation path is

C:\Cygnus. It contains all arm-tools and manuals. Just keep this folder for now and we will leave this feature for future.

Finally, from *Project→Rebuild all target files* we will be able to generate a hex code under the project folder. The next section describes the procedure to use LPC2000 FLASH UTILIY to download this execute file via Boot Loader, which is built-in LPC2103.



## LPC2000 FLASH UTILITY

To use the Boot Loader, one may connect a straight cable from J2 (standard DB-9 female header) onboard to one of the COM PORT of your PC. Make sure jumpers at JP1 & JP5 are present. Fail to do so will disable communication between LPC2000 Flash Utility Program and the evaluation board. Download the LPC2000 Flash Utility program from the following web site.

### http://www.nxp.com/products/microcontrollers/support/software\_download/lpc2000/

By installation and launching the program you will see the program interface below. *The actual parameters shown on the first program startup will be different. Just don't worry too much at the moment.* 

S LPC2000 Flash Utility	
	(0 0 0
	/2.2.3
Flash Programming Erase / Blank	Communication
Filename:       Image: Start Sector:         Upload to Flash       Image: Execute Code after Upload         Compare Flash       Manual Reset	Connected To Port COM1:  Use Baud Rate: 38400 Time-Out [sec]: 3
Device: PC2103/2/1  Read Part ID: Part ID: NTAL Freq. [kHz]: 12000 Boot Loader ID:	Use DTR/RTS for Reset and Boot Loader Selection
Running from Device Memory	

Check the check-box Use DTR/RTS for Reset and Boot Loader Selection at the lower right corner.

Enter the XTAL frequency as 12000. Our crystal frequency is 12MHz. Select the COM Port and baud rate. We are using COM1 as an example.

The choice of baud rate depends on XTAL Freq. From experience, a baud rate of 38400 is a good choice for 12MHz XTAL frequency; fast and stable.

Press the file browser button and browse to your target hex code. If you don't follow from last section, it is also possible to use our ready-made programs available at the following hyperlink. A proven project for 2.4" TFT LCD demo has been uploaded to the link Doc 06. The operation is the same for all kind of hex code.

http://www.techtoys.com.hk/ARM\_boards/LPC2103\_Eval\_1A/LPC2103\_Eval\_1A.htm.

Supply 5V DC with pin positive to the power jack, and press the button *Read Device ID*. You will see the Part ID and Boot Loader ID as shown below. These parameters show that the Flash Utility program has successfully hooked up with the microcontroller.

Si LPC2000 Flash Utility	
Elle Buffer Help LPC2000 Flash Utility V	2.2.3
Flash Programming       Filename:         D:\ARM\LPC2103\Keil\ILI9325P_8BIT Der          Upload to Flash       Image: Execute Code after Upload         Compare Flash       Manual Reset         Device       Erase         Device:       LPC2103/2/1         Device:       LPC2103/2/1         Manual Reset       Part ID: 327441         Device:       LPC2103/2/1         Manual Reset       Boot Loader ID: 22	Communication Connected To Port: CDM1: ▼ Use Baud Rate: 38400 ▼ Time-Out [sec]: 3 Use DTR/RTS for Reset and Boot Loader Selection
Read Part ID Successfully	

Follow the screen shot above for other parameters and finally press the *Upload to Flash* button for code download. You will see program running after just 1-2 seconds.

## FLASH MAGIC

If you don't want the LPC2000 Flash Utility, you may consider Flash Magic which can be obtained from the web site <u>http://www.flashmagictool.com/</u>. This is a freeware sponsored by NXP Semiconductors.

Download, install, and launch the application you will see the user interface as below.

Section 10 Section 19	E ONLY
File ISP Options Tools Help	
🖻 🖬 🍳 🗃 🍏 🗸 🕷 🔊 🛛	ब 🛛 😮 😂
Step 1 - Communications	Step 2 - Erase
COM Port COM 1	Erase block 0 (0x000000-0x000FFF)
Baud Rate: 38400	Erase block: 1 (0x001000-0x001FFF) Erase block: 2 (0x002000-0x002FFF)
Device: LPC2103	Erase block 3 (0x003000-0x003FFF)
Interface: None (ISP)	Erase block 5 (0x005000-0x005FFF)
Oscillator Freq. (MHz): 12	Erase all Flash+Code Fid Prot
Step 3 - Hex File Hex File: Modified: Unknown	more info
Step 4 - Options	Step 5 - Start
<ul> <li>✓ Verify after programming</li> <li>✓ Fill unused Flash</li> <li>✓ Gen block checksums</li> <li>✓ Execute</li> </ul>	Prot Start
Rotating, fully customizable, remotely updated In	emet links. Embed them in your
application! www.embeddedhints.com	•

To configure LPC2103 Evaluation board for Flash programming, follow the steps.

**Step 1** - Communications: specify the COM Port to use and Baud Rate. From my experience, a Baud Rate of 38400 usually gives a good result. Try different Baud Rate to suit your particular environment.

#### Step 2 – Erase: Enable Erase blocks used by Hex File

Step 3 – Hex File: Browse to your hex code, for example D:\ARM\LPC2103\Keil\ILI9320P\_8BIT Demo.hex

**Step 4** – Options: Enable **Verify after programming option**. In the main menu **Options – Advanced Options** dialog, select the **Hardware Config** tab, then check the **Use DTR and RTS to control RST and P0.14** check box. This option is dictated by the hardware design.

**Step 5** – Start! : Finally press start to program

